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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/558,360	11/25/2005	Takuma Hashimoto	P28811	3905
	7590 09/25/200 & BERNSTEIN, P.L.0	EXAMINER		
1950 ROLAND	CLARKE PLACE	KHOSRAVIANI, ARMAN		
RESTON, VA	20191		ART UNIT	PAPER NUMBER
			2818	
			NOTIFICATION DATE	DELIVERY MODE
			09/25/2008	ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

gbpatent@gbpatent.com pto@gbpatent.com

Advisory Action Before the Filing of an Appeal Brief

Application No.	Applicant(s)	
10/558,360	HASHIMOTO ET AL.	
Examiner	Art Unit	

	Allilali Kilosiavialii	2010	
The MAILING DATE of this communication appe	ars on the cover sheet with the o	correspondence add	ress
THE REPLY FILED <u>10 September 2008</u> FAILS TO PLACE THI	S APPLICATION IN CONDITION F	FOR ALLOWANCE.	
1. The reply was filed after a final rejection, but prior to or on application, applicant must timely file one of the following application in condition for allowance; (2) a Notice of Appetor Continued Examination (RCE) in compliance with 37 Coperiods:	replies: (1) an amendment, affidavi eal (with appeal fee) in compliance	t, or other evidence, v with 37 CFR 41.31; o	which places the r (3) a Request
a) The period for reply expiresmonths from the mailing	date of the final rejection.		
b) The period for reply expires on: (1) the mailing date of this A no event, however, will the statutory period for reply expire Is Examiner Note: If box 1 is checked, check either box (a) or (MONTHS OF THE FINAL REJECTION. See MPEP 706.07(the content of the period	ater than SIX MONTHS from the mailing b). ONLY CHECK BOX (b) WHEN THE).	g date of the final rejection FIRST REPLY WAS FI	on. LED WITHIN TWO
Extensions of time may be obtained under 37 CFR 1.136(a). The date have been filed is the date for purposes of determining the period of extunder 37 CFR 1.17(a) is calculated from: (1) the expiration date of the set forth in (b) above, if checked. Any reply received by the Office later may reduce any earned patent term adjustment. See 37 CFR 1.704(b). NOTICE OF APPEAL	ension and the corresponding amount hortened statutory period for reply origi	of the fee. The appropri- nally set in the final Offic	ate extension fee be action; or (2) as
2. The Notice of Appeal was filed on A brief in comp	liance with 37 CFR 41.37 must be	filed within two month	s of the date of
filing the Notice of Appeal (37 CFR 41.37(a)), or any exter Notice of Appeal has been filed, any reply must be filed wi AMENDMENTS			e appeal. Since a
3. The proposed amendment(s) filed after a final rejection, b	out prior to the date of filing a brief,	will not be entered be	cause
(a) ☐ They raise new issues that would require further cor		ΓE below);	
(b) They raise the issue of new matter (see NOTE below	**		
(c) ☐ They are not deemed to place the application in bet appeal; and/or	er form for appeal by materially rec	ducing or simplifying t	ne issues for
(d) ☐ They present additional claims without canceling a c	corresponding number of finally reig	ected claims.	
NOTE: (See 37 CFR 1.116 and 41.33(a)).			
4. The amendments are not in compliance with 37 CFR 1.12	21. See attached Notice of Non-Co	mpliant Amendment (PTOL-324).
5. Applicant's reply has overcome the following rejection(s):		,	,
6. Newly proposed or amended claim(s) 16,18 and 20 would		arate, timely filed ame	endment
canceling the non-allowable claim(s).		•	
7. For purposes of appeal, the proposed amendment(s): a) [how the new or amended claims would be rejected is prov The status of the claim(s) is (or will be) as follows:		I be entered and an e	xplanation of
Claim(s) allowed: <u>16,18 and 20</u> .			
Claim(s) objected to: Claim(s) rejected: <u>1-15,17,19 and 21-28</u> .			
Claim(s) withdrawn from consideration:			
AFFIDAVIT OR OTHER EVIDENCE			
 The affidavit or other evidence filed after a final action, but because applicant failed to provide a showing of good and was not earlier presented. See 37 CFR 1.116(e). 			
 The affidavit or other evidence filed after the date of filing entered because the affidavit or other evidence failed to o showing a good and sufficient reasons why it is necessary 	vercome <u>all</u> rejections under appea	al and/or appellant fail	s to provide a
10. The affidavit or other evidence is entered. An explanation	n of the status of the claims after e	ntry is below or attach	ed.
REQUEST FOR RECONSIDERATION/OTHER			
 11. The request for reconsideration has been considered but See Continuation Sheet. 12. Note the etterhed Information Displaceure Statement(s). 		i condition for allowan	ce because:
12. ☐ Note the attached Information <i>Disclosure Statement</i>(s). (13. ☐ Other:	F10/30/00/ Paper NO(\$)		
	/Steven Loke/		
	SPE, AU 2818		

Continuation of 11. does NOT place the application in condition for allowance because: As presently amended, claims 1, 11, and 22 fail to overcome a rejection based on the cited prior art Sugimoto and Harrah. Claim 1 was amended to incorporate rejected claim 2. Claim 11 was amended to incorporate rejected claim 1. Claim 22 was amended to incorporate rejected claim 21.

With respect to claim 1: As cited in the Advisory Action, the language of "the first plate of the mount base of said submount is bonded thermally to a portion of the metallic plate...by removing the insulator layer and the pattern layer" is considered a method of forming the device of claim 1 and not limitations of the final product. Therefore, such limitations are given no patentable weight. The combination of Sugimoto and Harrah teaches the final structure.

Harrah teaches electrically conducting lines 34, 36 of said submount (...comprising a mount base made of an electrically insulating material (e.g. ceramic), at least one light-emitting diode chip mounted thereon 28 and electrically conducting lines formed on the mount base to be connected electrically (top contacts, vias, solder and bottom contacts) to the light-emitting diode chip) are connected electrically to the electrical connection pattern layer 8 of said first plate (by vias 38, 40).

With respect to claim 11: Figure 4a of Sugimoto shows a mount base 4, with a groove formed thereon.

With respect to claim 22: metal traces 20 formed on second plate 12 between insulator layer 18 constitute an electrical connection pattern layer connected electrically to electrically conducting linesof said submount where submount has lines to IC 21.